



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/687,068
Applicant : Paul A. Reid et al.
Filed : October 16, 2003
Title : Single-Sensor Microcontroller-Based Approach
for Ground Fault Circuit Interrupters

TC/A.U. : Unassigned
Examiner : Unassigned

Docket No. : CRE-39 / 47181-00278USPT
Customer No. : 23569

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.97 AND 1.98

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING
37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below:

1-7-04
Date

Lynn A. Handrick

Dear Commissioner:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Information Disclosure Statement be entered and the reference listed on attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with the Patent and Trademark Office's waiver of the requirement under 37 C.F.R. § 1.98(a)(2)(i), a copy of the listed reference is not enclosed because the current application has been filed after June 30, 2003 and because the listed reference is a U.S. patent.

The applicants would like to make the Examiner aware of the following application that is being pursued by the assignee of the present application:

Application No.	Date Filed	Title
10/718,244	11/20/2003	Electrical Fault Detection System

In accordance with 37 C.F.R. § 1.98(a)(2)(iii), a copy of the above-referenced U.S. Patent Application Serial No. 10/718,244 is enclosed.

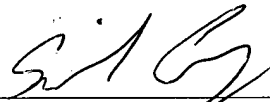
In accordance with 37 C.F.R. §§ 1.97(g) and (h), this Information Disclosure Statement is not to be construed as a representation that a search has been made, or an admission that the information disclosed is, or is considered to be, prior art with respect to the present application or material to patentability, as defined in 37 C.F.R. § 1.56.

The present Information Disclosure Statement is being filed within three months of the filing date of this application and hence is believed to be timely in accordance with 37 C.F.R. § 1.97(b). Accordingly, no fees are believed to be due in connection with the filing of this Information Disclosure Statement. However, should any fees be deemed necessary (except payment of the issue fee), the Commissioner is authorized to charge any deficiency or to credit any overpayment to Jenkins & Gilchrist Account No. 10-0447/47181-00278USPT.

Respectfully submitted,

Date: January 7, 2004

By



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Form PTO-1449 (modified)

Attorney Docket No.: CRE-39 / 47181-00278USPT

Serial No.: 10/687,068

List of Patents and Publications for Applicant's

Applicant: Paul A. Reid et al.

INFORMATION DISCLOSURE STATEMENT

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Filing Date: October 16, 2003

Group: Unassigned

U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	A01	4,053,815	10/11/1977	Sircom	361	42	
	A02						
	A03						
	A04						
	A05						
	A06						
	A07						
	A08						
	A09						
	A10						

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation

EXAMINER: _____ DATE CONSIDERED: _____

EXAMINER: INITIAL IF REFERENCE CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.